



Serial No:CLP216

**BIR TIKENDRAJIT UNIVERSITY**

Established Under Section 2(f) UGC Act, 1956

IMPHAL MANIPUR, INDIA

STATEMENTS OF MARKS**BACHELOR OF TECHNOLOGY (ELECTRONICS & COMMUNICATION ENGINEERING)**

Name: PILLI VIVEK

Seat No.: 2022031435966

Father's Name: PILLI BRAHMAIAH

Year/Sem: I

Enrollment No. : 202106910347076930

Month & Year Of Exam : January 2022

Paper Code	Paper Title	Internal Assessment		External Assessment		Total		Remarks
		Maximum Marks	Marks Secured	Maximum Marks	Marks Secured	Max Marks	Marks Secured	
101	MATHEMATICS-I	40	23	60	34	100	57	P
102	PHYSICS	40	21	60	45	100	66	P
103	BASIC ELECTRICAL ENGINEERING	40	26	60	41	100	67	P
104	ENGINEERING GRAPHICS	40	22	60	37	100	59	P
105	COMMUNICATION SKILLS	40	20	60	44	100	64	P
106	ENVIRONMENTAL SCIENCE	40	28	60	41	100	69	P
107	WORKSHOP PRACTICE	40	22	60	45	100	67	P
Grand Total						700	449	64.14%
Result								PASS

Remarks:P- Pass; F- Fail; A- Absent Gm Grace Marks

Date of issue: 14-Mar-2022



Controller of Examinations/Dy. Registrar



Serial No: UIP757

**BIR TIKENDRAJIT UNIVERSITY**

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IMPHAL MANIPUR, INDIA

STATEMENTS OF MARKS**BACHELOR OF TECHNOLOGY (ELECTRONICS & COMMUNICATION ENGINEERING)**

Name: PILLI VIVEK

Seat No.: 2022082526698

Father's Name: PILLI BRAHMAIAH

Year/Sem: II

Enrollment .No. : 202106910347076930

Month & Year Of Exam : July 2022

Paper Code	Paper Title	Internal Assessment		External Assessment		Total		Remarks
		Maximum Marks	Marks Secured	Maximum Marks	Marks Secured	Max Marks	Marks Secured	
201	MATHEMATICS-II	40	25	60	41	100	66	P
202	BASIC ELECTRONICS ENGINEERING	40	22	60	37	100	59	P
203	PROGRAMMING IN C	40	27	60	44	100	71	P
204	ENGINEERING MECHANICS	40	22	60	40	100	62	P
205	CHEMISTRY	40	28	60	42	100	70	P
206	C PROGRAMMING LAB	40	22	60	45	100	67	P
Grand Total						600	395	65.83%
Result								PASS

Remarks: P- Pass; F- Fail; A- Absent Gm Grace Marks

Date of issue: 25-Aug-2022



Controller of Examinations/Dy. Registrar



Serial No:AKR626

**BIR TIKENDRAJIT UNIVERSITY**

Established Under Section 2(f) UGC Act, 1956

IMPHAL MANIPUR, INDIA

STATEMENTS OF MARKS**BACHELOR OF TECHNOLOGY (ELECTRONICS & COMMUNICATION
ENGINEERING)**

Name: PILLI VIVEK

Seat No.: 2023041077320

Father's Name: PILLI BRAHMAIAH

Year/Sem: III

Enrollment .No. : 202106910347076930

Month & Year Of Exam : January 2023

Paper Code	Paper Title	Internal Assessment		External Assessment		Total		Remarks
		Maximum Marks	Marks Secured	Maximum Marks	Marks Secured	Max Marks	Marks Secured	
301	DIGITAL ELECTRONICS	40	25	60	34	100	59	P
302	NETWORK ANALYSIS & SYNTHESIS	40	21	60	45	100	66	P
303	ELECTRONIC DEVICES	40	28	60	41	100	69	P
304	SIGNALS & SYSTEMS	40	22	60	38	100	60	P
305	DATA STRUCTURES	40	26	60	42	100	68	P
306	DIGITAL ELECTRONICS LAB	40	22	60	45	100	67	P
Grand Total						600	389	64.83%
Result								PASS

Remarks:P- Pass; F- Fail; A- Absent Gm Grace Marks

Date of issue: 10-Apr-2023



Controller of Examinations/Dy. Registrar



Serial No:LUC755

**BIR TIKENDRAJIT UNIVERSITY**

Established Under Section 2(f) UGC Act, 1956

IMPHAL MANIPUR, INDIA

STATEMENTS OF MARKS**BACHELOR OF TECHNOLOGY (ELECTRONICS & COMMUNICATION ENGINEERING)**

Name: PILLI VIVEK

Seat No.: 2023072598653

Father's Name: PILLI BRAHMAIAH

Year/Sem: IV

Enrollment .No. : 202106910347076930

Month & Year Of Exam : July 2023

Paper Code	Paper Title	Internal Assessment		External Assessment		Total		Remarks
		Maximum Marks	Marks Secured	Maximum Marks	Marks Secured	Max Marks	Marks Secured	
401	ANALOG CIRCUITS	40	25	60	34	100	59	P
402	MICROPROCESSORS	40	20	60	45	100	65	P
403	ELECTROMAGNETIC FIELD THEORY	40	21	60	38	100	59	P
404	CONTROL SYSTEMS	40	26	60	42	100	68	P
405	PROBABILITY & RANDOM PROCESSES	40	22	60	47	100	69	P
406	ANALOG CIRCUITS LAB	40	28	60	44	100	72	P
Grand Total						600	392	65.33%
Result								PASS

Remarks:P- Pass; F- Fail; A- Absent Gm Grace Marks

Date of issue: 25-Jul-2023



Controller of Examinations/Dy. Registrar



Serial No:RIB667

**BIR TIKENDRAJIT UNIVERSITY**

Established Under Section 2(f) UGC Act, 1956

IMPHAL MANIPUR, INDIA

STATEMENTS OF MARKS**BACHELOR OF TECHNOLOGY (ELECTRONICS & COMMUNICATION ENGINEERING)**

Name: PILLI VIVEK

Seat No.: 2024030135124

Father's Name: PILLI BRAHMAIAH

Year/Sem: V

Enrollment .No. : 202106910347076930

Month & Year Of Exam : January 2024

Paper Code	Paper Title	Internal Assessment		External Assessment		Total		Remarks
		Maximum Marks	Marks Secured	Maximum Marks	Marks Secured	Max Marks	Marks Secured	
501	DIGITAL COMMUNICATION	40	22	60	37	100	59	P
502	ANALOG COMMUNICATION	40	21	60	45	100	66	P
503	MICROCONTROLLERS & EMBEDDED SYSTEMS	40	27	60	41	100	68	P
504	ANTENNA & WAVE PROPAGATION	40	22	60	34	100	56	P
505	VLSI DESIGN	40	28	60	42	100	70	P
506	COMMUNICATION LAB	40	22	60	45	100	67	P
Grand Total						600	386	64.33%
Result								PASS

Remarks:P- Pass; F- Fail; A- Absent Gm Grace Marks

Date of issue: 01-Mar-2024



Controller of Examinations/Dy. Registrar



Serial No:PLS711

**BIR TIKENDRAJIT UNIVERSITY**

Established Under Section 2(f) UGC Act, 1956

IMPHAL MANIPUR, INDIA

STATEMENTS OF MARKS**BACHELOR OF TECHNOLOGY (ELECTRONICS & COMMUNICATION ENGINEERING)**

Name: PILLI VIVEK

Seat No.: 2024071535623

Father's Name: PILLI BRAHMAIAH

Year/Sem: VI

Enrollment No. : 202106910347076930

Month & Year Of Exam : June 2024

Paper Code	Paper Title	Internal Assessment		External Assessment		Total		Remarks
		Maximum Marks	Marks Secured	Maximum Marks	Marks Secured	Max Marks	Marks Secured	
601	DIGITAL SIGNAL PROCESSING (DSP)	40	26	60	32	100	58	P
602	MICROWAVE ENGINEERING	40	26	60	44	100	70	P
603	WIRELESS COMMUNICATION	40	26	60	41	100	67	P
604	COMPUTER NETWORKS	40	22	60	45	100	67	P
605	INDUSTRIAL TRAINING	40	20	60	48	100	68	P
606	DSP LAB	40	25	60	47	100	72	P
Grand Total						600	402	67.00%
Result								PASS

Remarks:P- Pass; F- Fail; A- Absent Gm Grace Marks

Date of issue: 15-Jul-2024



Controller of Examinations/Dy. Registrar



Serial No:ZSR081

**BIR TIKENDRAJIT UNIVERSITY**

Established Under Section 2(f) UGC Act, 1956

IMPHAL MANIPUR, INDIA

STATEMENTS OF MARKS**BACHELOR OF TECHNOLOGY (ELECTRONICS & COMMUNICATION ENGINEERING)**

Name: PILLI VIVEK

Seat No.: 2025021086962

Father's Name: PILLI BRAHMAIAH

Year/Sem: VII

Enrollment .No. : 202106910347076930

Month & Year Of Exam : January 2025

Paper Code	Paper Title	Internal Assessment		External Assessment		Total		Remarks
		Maximum Marks	Marks Secured	Maximum Marks	Marks Secured	Max Marks	Marks Secured	
701	OPTICAL COMMUNICATION	40	26	60	34	100	60	P
702	MOBILE COMMUNICATION	40	21	60	45	100	66	P
703	INFORMATION THEORY & CODING	40	28	60	42	100	70	P
704	POWER ELECTRONICS	40	22	60	37	100	59	P
705	OPEN ELECTIVE: ARTIFICIAL INTELLIGENCE	40	25	60	44	100	69	P
706	PROJECT PHASE-I	40	23	60	41	100	64	P
Grand Total						600	388	64.67%
Result								PASS

Remarks:P- Pass; F- Fail; A- Absent Gm Grace Marks

Date of issue: 10-Feb-2025



Controller of Examinations/Dy. Registrar



Serial No:AVX221

**BIR TIKENDRAJIT UNIVERSITY**

Established Under Section 2(f) UGC Act, 1956

IMPHAL MANIPUR, INDIA

STATEMENTS OF MARKS**BACHELOR OF TECHNOLOGY (ELECTRONICS & COMMUNICATION ENGINEERING)**

Name: PILLI VIVEK

Seat No.: 2025070724884

Father's Name: PILLI BRAHMAIAH

Year/Sem: VIII

Enrollment .No. : 202106910347076930

Month & Year Of Exam : June 2025

Paper Code	Paper Title	Internal Assessment		External Assessment		Total		Remarks
		Maximum Marks	Marks Secured	Maximum Marks	Marks Secured	Max Marks	Marks Secured	
801	SATELLITE COMMUNICATION	40	24	60	34	100	58	P
802	IOT SYSTEMS	40	20	60	41	100	61	P
803	ELECTIVES: EMBEDDED IOT	40	26	60	33	100	59	P
804	PROJECT PHASE-II	40	22	60	40	100	62	P
Grand Total						400	240	60.00%
Result								PASS

Remarks:P- Pass; F- Fail; A- Absent Gm Grace Marks

Date of issue: 07-Jul-2025



Controller of Examinations/Dy. Registrar